



Low Temperature Electronics

L. Brogiato, D. V. Camin, G. Pessina



Low Temperature Electronics:

Low Temperature Electronics Edmundo A. Gutierrez-D., M. Jamal Deen, Cor L. Claeys, 2001 Summarizes the advances in cryoelectronics starting from the fundamentals in physics and semiconductor devices to electronic systems hybrid superconductor semiconductor technologies photonic devices cryocoolers and thermal management This book provides an exploration of the theory research and technologies related to cryoelectronics *Device and Circuit Cryogenic Operation for Low Temperature Electronics* Francis Balestra, Gérard Ghibaudo, 2001-05-31 *Device and Circuit Cryogenic Operation for Low Temperature Electronics* is a first in reviewing the performance and physical mechanisms of advanced devices and circuits at cryogenic temperatures that can be used for many applications The first two chapters cover bulk silicon and SOI MOSFETs The electronic transport in the inversion layer the influence of impurity freeze out the special electrical properties of SOI structures the device reliability and the interest of a low temperature operation for the ultimate integration of silicon down to nanometer dimensions are described The next two chapters deal with Silicon Germanium and III V Heterojunction Bipolar Transistors as well as III V High Electron Mobility Transistors HEMT The basic physics of the SiGe HBT and its unique cryogenic capabilities the optimization of such bipolar devices and the performance of SiGe HBT BiCMOS technology at liquid nitrogen temperature are examined The physical effects in III V semiconductors at low temperature the HEMT and HBT static high frequency and noise properties and the comparison of various cooled III V devices are also addressed The next chapter treats quantum effect devices made of silicon materials The major quantum effects at low temperature quantum wires quantum dots as well as single electron devices and applications are investigated The last chapter overviews the performances of cryogenic circuits and their applications The low temperature properties and performance of inverters multipliers adders operational amplifiers memories microprocessors imaging devices circuits and systems sensors and read out circuits are analyzed *Device and Circuit Cryogenic Operation for Low Temperature Electronics* is useful for researchers engineers Ph D and M S students working in the field of advanced electron devices and circuits new semiconductor materials and low temperature electronics and physics [Low-temperature Electronics](#) Randall K. Kirschman, 1986 *Low temperature electronics : physics, services, circuits and application* Edmundo Gutierrez, 2000 *Low-Temperature Electronics* Kirschman RK Ed, 1986 [Low Temperature Electronics and Low Temperature Cofired Ceramic Based Electronic Devices](#) Electrochemical Society. Meeting, 2004 **Electrical Devices and Circuits for Low Temperature Space Applications** National Aeronautics and Space Administration (NASA), 2018-06-20 The environmental temperature in many NASA missions such as deep space probes and outer planetary exploration is significantly below the range for which conventional commercial off the shelf electronics is designed Presently spacecraft operating in the cold environment of such deep space missions carry a large number of radioisotope or other heating units in order to maintain the surrounding temperature of the on board electronics at approximately 20 C Electronic devices and circuits capable of operation at

cryogenic temperatures will not only tolerate the harsh environment of deep space but also will reduce system size and weight by eliminating or reducing the heating units and their associated structures thereby reducing system development cost as well as launch costs. In addition, power electronic circuits designed for operation at low temperatures are expected to result in more efficient systems than those at room temperature. This improvement results from better behavior in the electrical and thermal properties of some semiconductor and dielectric materials at low temperatures. An ongoing research and development program on low temperature electronics at the NASA Glenn Research Center focuses on the development of efficient electrical systems and circuits capable of surviving and exploiting the advantages of low temperature environments. An overview of the program will be presented in this paper. A description of the low temperature test facilities along with selected data obtained from in-house component testing will also be discussed. Ongoing research activities that are being performed in collaboration with various organizations will also be presented.

Patterson R L and Hammond A and Dickman J E and Gerber S and Overton E and Elbuluk M Glenn Research Center Goddard Space Flight Center NASA TM 2003 212600 E 14159 NAS 1 15 212600 *Electronics Program. Low Temperature Electronics* F. JR. VERNON, DEPUTY COMMANDER AEROSPACE SYSTEMS INGLEWOOD CALIF., 1962 **Development of Electronics for Low-temperature Space Missions** Richard L. Patterson, 2001 Proceedings of the Symposium on Low Temperature Electronics and High Temperature Superconductors, 1988 *Low Temperature Electronics: Physics, Devices, Circuits And Applications* Edmundo A. Deen Gutierrez-d., **Proceedings of the Symposium on Low Temperature Electronic Device Operation** Daniel Foty, 1991 *Proceedings of the Fourth Symposium on Low Temperature Electronics and High Temperature Superconductivity* Cor L. Claeys, 1997 **Low-temperature Electronics** L. Brogiato, D. V. Camin, G. Pessina, *Proceedings of the Symposium on Low Temperature Electronics and High Temperature Superconductivity* Cor L. Claeys, 1995 **Electronics Research Program. Low-temperature Electronics** F. L. Vernon (Jr), AEROSPACE CORP EL SEGUNDO CALIF., 1963 Research in low temperature electronics consisted primarily of measurement of the performance of critical current critical field $I_c H_c$ characteristics of annealed niobium wire specimens. In the experiments performed the peak effect in the $I_c H_c$ characteristics occurred only for H perpendicular to the sample axis and the current flow and only for samples which were annealed at temperatures of 1400 C and above. For fields parallel to the current an anomalous resistance was noticed for fields and currents well within the values which usually allow the sample to be superconducting. This island effect to date does not have a satisfactory explanation. Correlation of the field at which the peak effect occurs and the H_{c2} of the Abrikosov theory show satisfactory agreement and indicate that niobium is intrinsically a superconductor of the second type. Some limited experimental work was performed on thin filamentary superconductors. Author Special Issue: Workshop on Low Temperature Electronics 208 Michael Siegel, 2009 Proceedings of the Symposium on Low Temperature Electronics and High Temperature Superconductors Held in Honolulu, Hawaii on 19-23 October 1987 Stanley I.

Raider, Randall Kirschman, Hisao Hayakawa, Hiroshi Ohta, ELECTROCHEMICAL SOCIETY INC PENNINGTON NJ., 1988 The included papers demonstrate that the study and use of low temperature electronics are increasing in many diverse areas providing valuable benefits for many applications as well as opportunities for research in materials and devices Operating electronic devices and systems at low temperatures present many challenges for the designer and user Although there is active interest in this area it remains to be seen whether the advantages achieved by cooling electronics will lead to widespread practice Two recent developments both discussed in this volume have particularly focused attention on the field of low temperature electronics and could impact future applications First a supercomputer the ETA 10 with its high speed VLSI processors populated with CMOS and cooled to 77 K during operation was delivered to Florida State University and is presently being field tested as it is brought up to its full operating potential Secondly superconductors with critical temperatures T_c in excess of 90 K were prepared by Chu et al following the pioneering work of Mueller and Bednorz at the IBM Zurich Research Laboratory Superconductivity can now be achieved at the same temperatures and with the same refrigerators used to cool semiconductor electronics mjm Low Temperature Electronics (WOLTE), 2014 11th International Workshop on, 2014 **Low Temperature Physics** William Duffy, James A. Miller, 1970

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