

Integrated Circuit



Models For Large Integrated Circuits

Robert B. Northrop



Models For Large Integrated Circuits:

Models for Large Integrated Circuits Patrick DeWilde, Zhen-Qiu Ning, 2012-12-06 A modern microelectronic circuit can be compared to a large construction a large city on a very small area A memory chip a DRAM may have up to 64 million bit locations on a surface of a few square centimeters Each new generation of integrated circuit generations are measured by factors of four in overall complexity requires a substantial increase in density from the current technology added precision a decrease of the size of geometric features and an increase in the total usable surface The microelectronic industry has set the trend Ultra large funds have been invested in the construction of new plants to produce the ultra large scale circuits with utmost precision under the most severe conditions The decrease in feature size to submicrons 0.7 micron is quickly becoming available does not only bring technological problems New design problems arise as well The elements from which microelectronic circuits are built transistors and interconnects have different shape and behave differently than before Phenomena that could be neglected in a four micron technology such as the non uniformity of the doping profile in a transistor or the mutual capacitance between two wires now play an important role in circuit design This situation does not make the life of the electronic designer easier he has to take many more parasitic effects into account up to the point that his ideal design will not function as originally planned

Modeling of Integrated Circuits as Components of Large Systems D. N. Pockock, Air Force Weapons Laboratory, M. G. Krebs, J. P. Raymond, Northrop Corporate Laboratories, W. W. Chang, NORTHROP CORPORATE LABS HAWTHORNE CA., 1972 Results are presented on generalized approaches to derive radiation inclusive simplified models of linear and digital microcircuits Application of the principle of superposition allows generation of a compact small signal model of the linear microcircuit with extension of the model to include large signal saturation effects The digital microcircuit model is the combination of current voltage terminal networks with a logical decision function to represent the truth table of the device The techniques are used to generate terminal models of an integrated circuit operational amplifier voltage comparator and J K flip flop A factor of 20 to 50 improvement in required computer time and storage was realized with the terminal models over detailed models with no significant loss in the representation of key performance parameters or radiation environment vulnerability Preliminary modeling results on the phase locked loop nonlinear analog microcircuit are also presented Author

Integrated Circuit and System Design. Power and Timing Modeling, Optimization and Simulation Johan Vounckx, Nadine Azemard, Philippe Maurine, 2006-09-07 This book constitutes the refereed proceedings of the 16th International Workshop on Power and Timing Modeling Optimization and Simulation PATMOS 2006 The book presents 41 revised full papers and 23 revised poster papers together with 4 key notes and 3 industrial abstracts Topical sections include high level design power estimation and modeling memory and register files low power digital circuits busses and interconnects low power techniques applications and SoC design modeling and more

Computer-Aided Design of Analog Integrated Circuits and Systems Rob A.

Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck

Scientific and Technical Aerospace Reports ,1985 NASA Thesaurus ,1998 Contains the authorized subject terms by which the documents in the NASA STI Database are indexed and retrieved *Analysis and Design of Analog Integrated Circuits* Paul R. Gray, Robert G. Meyer, 1977 This edition combines the consideration of metal oxide semiconductors MOS and bipolar circuits into a unified treatment that also includes MOS bipolar connections made possible by BiCMOS technology Contains extensive use of SPICE especially as an integral part of many examples in the problem sets as a more accurate check on hand calculations and as a tool to examine complex circuit behavior beyond the scope of hand analysis Concerned largely with the design of integrated circuits a considerable amount of material is also included on applications

MOSFET Models for VLSI Circuit Simulation Narain D. Arora, 2012-12-06 Metal Oxide Semiconductor MOS transistors are the basic building block of MOS integrated circuits I C Very Large Scale Integrated VLSI circuits using MOS technology have emerged as the dominant technology in the semiconductor industry Over the past decade the complexity of MOS IC s has increased at an astonishing rate This is realized mainly through the reduction of MOS transistor dimensions in addition to the improvements in processing Today VLSI circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0.5 microns are in volume production Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator This problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models There is extensive literature available on modeling these short channel devices

However there is a lot of confusion too Often it is not clear what model to use and which model parameter values are important and how to determine them After working over 15 years in the field of semiconductor device modeling I have felt the need for a book which can fill the gap between the theory and the practice of MOS transistor modeling This book is an attempt in that direction

Timing Models for MOS Circuits Stanford University. Stanford Electronics Laboratories. Integrated Circuits Laboratory, Mark Alan Horowitz, 1983

Models for a Comprehensive Methodology for Parameter Selection and Discrete Decision-making in the Design of Integrated Circuits David Charles Riley, 1988 **IEEE Circuits & Devices**, 1998

The Physics and Modeling of Latch-up and CMOS Integrated Circuits Donald B. Estreich, Stanford University. Stanford Electronics Laboratories. Integrated Circuits Laboratory, 1980

Analog Electronic Circuits Robert B. Northrop, 1990

Analog Integrated Circuits Joseph Alvin Connelly, 1975 A young chimp enumerates favorite playtime activities from painting and riding a bike to paddling in the sea and partying with friends

Radio Electronics and Communications Systems, 1988

Midwest Symposium on Circuits and Systems, 1985

XII Symposium on Integrated Circuits and Systems Design Sociedade Brasileira de Computação, IFIP WG 10.5, 1999 Topics in these conference papers include microprocessors design modelling co design analog design high level synthesis digital design synthesis and reconfiguration CAD tools and IP cores

A Methodology for Modeling the Manufacturability of Integrated Circuits Eric David Boskin, 1995

Proceedings of the ... Custom Integrated Circuits Conference, 1987

Prototype-device Fabrication and Modeling for All-semiconductor Three-dimensional Integrated Circuits Ronald David Schrimpf, 1986

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