OPTIMIZING UNIX
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AMIR H. MAJIDIMEHR

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High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures K. J. Ray Liu,Kung Yao,1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today s rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures *Local Area Networks* Thomas W. Madron,1990-06-04 Shows what LANs are and how they work with guidelines for choosing designing and implementing a microcomputer LAN **Protocols for High-speed Networks** Harry Rudin,Robin Williamson,1989 The purpose of this book is to discuss some of the issues involved in the design and implementation of protocols particularly protocols above the medium access layer that are able to exploit the full potential of local area and optical fiber networks The main issue in this area continues to be whether or not new protocols are necessary On the one hand a number of new high speed protocols have been invented On the other hand some recent work shows that clever tuning and implementation of existing protocol architectures can also deliver high throughput and quick response times The papers presented in this book shed some light on the issue

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