

Multiple-Valued Logic in VLSI: Challenges and Opportunities

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Abstract

In recent years, there have been major advances in integrated circuit technology which have both made feasible and generated great interest in electronic circuits which employ more than two discrete levels of signal. Such circuits, called multiple-valued logic circuits, offer several potential opportunities for the improvement of present VLSI circuit designs. In this paper, we give an overview of recent developments in multiple-valued logic circuit design, revealing both the opportunities they offer and the challenges they face.

1 Introduction

When people ask me why I am doing research in multiple-valued logic, I often reply that it is like painting a picture having all possible colors available. Once you tried them, you will never return to just black and white. Multiple-valued logic displays us phenomena, we would never see in binary case, where the only two values available are *null* and *unit* elements of Boolean algebra¹, possessing very specific properties. Reflected back to two-valued scale, these phenomena give us a new, deeper understanding of the matter.

Apart from giving us a better insight into binary problems, multiple-valued logic has many other useful applications. They can be classified into two groups. The first group uses multiple-valued logic domain to solve binary problems more efficiently. For example, a well-known approach to represent a multiple-output Boolean function is to convert it to a single-output multiple-valued function, by treating its output part as a single multiple-valued variable. Such an approach is used, for instance, in Berkeley's tool for verification and synthesis VIS [1].

The second group targets the design of electronic circuits which employ more than two discrete levels of signals, such as multiple-valued memories, arithmetic circuits, Field Programmable Gate Arrays etc. Multiple-valued logic (MVL) circuits offer several potential opportunities for the improvement of present VLSI circuit designs. For example, serious difficulties with limitations on the number of connections of an integrated circuit with the

¹The *null* $\mathbf{0}$ and *unit* $\mathbf{1}$ elements of an algebra over a set A have the properties that, for each $x \in A$, $x + \mathbf{0} = x$ and $x \cdot \mathbf{1} = x$.

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