



# Signal Routing In Integrated Circuit Layout Computer Science

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## **Signal Routing In Integrated Circuit Layout Computer Science:**

**Signal Routing in Integrated Circuit Layout** Chi-Ping Hsu,1986      **Mixed-Signal Layout Generation Concepts** Chieh Lin,Arthur H.M. van Roermund,Domine Leenaerts,2005-12-15 This title covers important physical design issues that exist in contemporary analogue and mixed signal design flows The authors bring together many principles and techniques required to successfully develop and implement layout generation tools to accommodate many mixed signal layout generation needs      VLSI Circuit Layout Te Chiang Hu,Ernest S. Kuh,1985      *Scientific and Technical Aerospace Reports* ,1990  
Layout Design and Verification Tatsuo Ohtsuki,1986 Very Good No Highlights or Markup all pages are intact      **Recent Awards in Engineering** ,1983      **Bibliographic Guide to Technology** New York Public Library. Research Libraries,1978  
Automated Performance Optimization of Custom Integrated Circuits Stephen Trimmerger,1986      **Dissertation Abstracts International** ,1999      1991 IEEE International Symposium on Circuits and Systems ,1991      **Subject Guide to Books in Print** ,1996      *Analog Layout Generation for Performance and Manufacturability* Koen Lampaert,Georges Gielen,Willy Sansen,1999-04-30 Analog integrated circuits are very important as interfaces between the digital parts of integrated electronic systems and the outside world A large portion of the effort involved in designing these circuits is spent in the layout phase Whereas the physical design of digital circuits is automated to a large extent the layout of analog circuits is still a manual time consuming and error prone task This is mainly due to the continuous nature of analog signals which causes analog circuit performance to be very sensitive to layout parasitics The parasitic elements associated with interconnect wires cause loading and coupling effects that degrade the frequency behaviour and the noise performance of analog circuits Device mismatch and thermal effects put a fundamental limit on the achievable accuracy of circuits For successful automation of analog layout advanced place and route tools that can handle these critical parasitics are required In the past automatic analog layout tools tried to optimize the layout without quantifying the performance degradation introduced by layout parasitics Therefore it was not guaranteed that the resulting layout met the specifications and one or more layout iterations could be needed In *Analog Layout Generation for Performance and Manufacturability* the authors propose a performance driven layout strategy to overcome this problem In this methodology the layout tools are driven by performance constraints such that the final layout with parasitic effects still satisfies the specifications of the circuit The performance degradation associated with an intermediate layout solution is evaluated at runtime using predetermined sensitivities In contrast with other performance driven layout methodologies the tools proposed in this book operate directly on the performance constraints without an intermediate parasitic constraint generation step This approach makes a complete and sensible trade off between the different layout alternatives possible at runtime and therefore eliminates the possible feedback route between constraint derivation placement and layout extraction Besides its influence on the performance layout also has a profound impact on the yield and testability of an analog circuit In *Analog Layout Generation for*

Performance and Manufacturability the authors outline a new criterion to quantify the detectability of a fault and combine this with a yield model to evaluate the testability of an integrated circuit layout They then integrate this technique with their performance driven routing algorithm to produce layouts that have optimal manufacturability while still meeting their performance specifications Analog Layout Generation for Performance and Manufacturability will be of interest to analog engineers researchers and students

**Algorithms for Integrated Circuit Signal Routing** Alan Edward Baratz,1981  
*IEEE Transactions on Circuits and Systems* ,2006     The Cumulative Book Index ,1988 A world list of books in the English language

**Program and Papers** ,1989     Comprehensive Dissertation Index ,1989     *Peterson's Guide to Graduate Programs in Engineering and Applied Sciences* ,1991     Telecommunications Abstracts ,1990     *Proceedings of the ... ACM Great Lakes Symposium on VLSI* ,2003

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