

**IEEE Symposium on
FIELD-
PROGRAMMABLE
CUSTOM
COMPUTING
MACHINES**

**20–23 April 2004
Napa, California**

Edited by John A. Abraham and Bernard L. Pong

Sponsored by IEEE Computer Society Technical Committee on Computer Architecture



Proceedings Ieee Symposium On Field Programmable Custom Computing Machines 12th 2004

Jiyuan Zhang



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FCCM 2004 Jeffrey M. Arnold, Kenneth L. Pocek, 2004-01-01 FCCM presents recent work on the use of reconfigurable logic as computing elements The proceedings focuses on topics such as device architecture system architecture compilation and programming tools run time environments nano technology and applications **Architecture of Computing Systems - ARCS 2006** Werner Grass, Bernhard Sick, Klaus Waldschmidt, 2006-02-15 This book constitutes the refereed proceedings of the 19th International Conference on Architecture of Computing Systems ARCS 2006 held in March 2006 The 32 revised full papers presented together with two invited and keynote papers were carefully reviewed and selected from 174 submissions The papers are organized in topical sections on pervasive computing memory systems architectures multiprocessing energy efficient design power awareness network protocols security and distributed networks Handbook of FPGA Design Security Ted Huffmire, Cynthia Irvine, Thuy D. Nguyen, Timothy Levin, Ryan Kastner, Timothy Sherwood, 2010-06-18 The purpose of this book is to provide a practical approach to managing security in FPGA designs for researchers and practitioners in the electronic design automation EDA and FPGA communities including corporations industrial and government research labs and academics This book combines theoretical underpinnings with a practical design approach and worked examples for combating real world threats To address the spectrum of lifecycle and operational threats against FPGA systems a holistic view of FPGA security is presented from formal top level specification to low level policy enforcement mechanisms which integrates recent advances in the fields of computer security theory languages compilers and hardware The net effect is a diverse set of static and runtime techniques that working in cooperation facilitate the composition of robust dependable and trustworthy systems using commodity components We wish to acknowledge the many people who helped us ensure the success of our work on reconfigurable hardware security In particular we wish to thank Andrei Paun and Jason Smith of Louisiana Tech University for providing us with a Linux compatible version of Grail We also wish to thank those who gave us comments on drafts of this book including Marco Platzner of the University of Paderborn and Ali Irturk and Jason Oberg of the University of California San Diego This research was funded in part by National Science Foundation Grant CNS 0524771 and NSF Career Grant CCF 0448654 *Partial Reconfiguration on FPGAs* Dirk Koch, 2012-07-25 This is the first book to focus on designing run time reconfigurable systems on FPGAs in order to gain resource and power efficiency as well as to improve speed Case studies in partial reconfiguration guide readers through the FPGA jungle straight toward a working system The discussion of partial reconfiguration is comprehensive and practical with models introduced together with methods to implement efficiently the corresponding systems Coverage includes concepts for partial module integration and corresponding communication architectures floorplanning of the on FPGA resources physical implementation aspects starting from constraining primitive placement and routing all the way down to the bitstream required to configure the FPGA and verification of reconfigurable systems *Embedded Computer Systems: Architectures, Modeling, and Simulation* Timo

D. Hämäläinen,2005-07-04 This book constitutes the refereed proceedings of the 5th International Workshop on Systems Architectures Modeling and Simulation SAMOS 2005 held in Samos Greece in July 2005 The 49 revised full papers presented were thoroughly reviewed and selected from 114 submissions The papers are organized in topical sections on reconfigurable system design and implementations processor architectures design and simulation architectures and implementations system level design and modeling and simulation *High-Performance Computing Using FPGAs* Wim Vanderbauwhede,Khaled Benkrid,2013-08-23 High Performance Computing using FPGA covers the area of high performance reconfigurable computing HPRC This book provides an overview of architectures tools and applications for High Performance Reconfigurable Computing HPRC FPGAs offer very high I O bandwidth and fine grained custom and flexible parallelism and with the ever increasing computational needs coupled with the frequency power wall the increasing maturity and capabilities of FPGAs and the advent of multicore processors which has caused the acceptance of parallel computational models The Part on architectures will introduce different FPGA based HPC platforms attached co processor HPRC architectures such as the CHREC s Novo G and EPCC s Maxwell systems tightly coupled HPRC architectures e g the Convey hybrid core computer reconfigurably networked HPRC architectures e g the QPACE system and standalone HPRC architectures such as EPFL s CONFETTI system The Part on Tools will focus on high level programming approaches for HPRC with chapters on C to Gate tools such as Impulse C AutoESL Handel C MORA C Graphical tools MATLAB Simulink NI LabVIEW Domain specific languages languages for heterogeneous computing for example OpenCL Microsoft s Kiwi and Alchemy projects The part on Applications will present case from several application domains where HPRC has been used successfully such as Bioinformatics and Computational Biology Financial Computing Stencil computations Information retrieval Lattice QCD Astrophysics simulations Weather and climate modeling **Dynamic Reconfigurable Architectures and Transparent Optimization Techniques** Antonio Carlos Schneider Beck Fl.,Luigi Carro,2010-03-10 Dynamic Reconfigurable Architectures and Transparent Optimization Techniques presents a detailed study on new techniques to cope with the aforementioned limitations First characteristics of reconfigurable systems are discussed in details and a large number of case studies is shown Then a detailed analysis of several benchmarks demonstrates that such architectures need to attack a diverse range of applications with very different behaviours besides supporting code compatibility This requires the use of dynamic optimization techniques such as Binary Translation and Trace reuse Finally works that combine both reconfigurable systems and dynamic techniques are discussed and a quantitative analysis of one them the DIM architecture is presented Reconfigurable Computing Scott Hauck,André DeHon,2010-07-26 Reconfigurable Computing marks a revolutionary and hot topic that bridges the gap between the separate worlds of hardware and software design the key feature of reconfigurable computing is its groundbreaking ability to perform computations in hardware to increase performance while retaining the flexibility of a software solution Reconfigurable computers serve as affordable fast and accurate tools for

developing designs ranging from single chip architectures to multi chip and embedded systems Scott Hauck and Andre DeHon have assembled a group of the key experts in the fields of both hardware and software computing to provide an introduction to the entire range of issues relating to reconfigurable computing FPGAs field programmable gate arrays act as the computing vehicles to implement this powerful technology Readers will be guided into adopting a completely new way of handling existing design concerns and be able to make use of the vast opportunities possible with reconfigurable logic in this rapidly evolving field Designed for both hardware and software programmers Views of reconfigurable programming beyond standard programming languages Broad set of case studies demonstrating how to use FPGAs in novel and efficient ways

Proceedings ,2005 **Recent Advances in Intrusion Detection** Somesh Jha,Robin Sommer,Christian Kreibich,2010-09-02 This book constitutes the refereed proceedings of the 13th International Symposium on Recent Advances in Intrusion Detection RAID 2010 held in Ottawa Canada in September 2010 The papers are organized in topical sections on network protection high performance malware detection and defence evaluation forensics anomaly detection as well as web security FCCM 2004 Jeffrey Arnold,Kenneth L. Pocek,2004 FCCM presents recent work on the use of reconfigurable logic as computing elements The proceedings focuses on topics such as device architecture system architecture compilation and programming tools run time environments nano technology and applications *Conference Proceedings* ,2006 **FPGA ...** ,2007 *Proceedings of Technical Papers* ,2005 *Proceedings of the ... ACM Great Lakes Symposium on VLSI.* ,2007 **VLSI Circuits and Systems** ,2005 **High-level Area Estimation for Customizable Array of Processors** Adam Douglas Harbour,2007 BORPH: an Operating System for FPGA-based Reconfigurable Computers Hayden Kwok-Hay So,2007 **Proceedings of the 2008 CGO** ,2008 **IEEE Symposium on FPGAs for Custom Computing Machines** Kenneth L. Pocek,Jeffrey M. Arnold,1996 Contains papers from the April 1996 symposium reporting on research in device architectures applications compilers and related software support Topics include using MORRPH in an industrial machine vision system a software development system for FPGA based data acquisition systems a reconfigura

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