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Network Processor Design Issues And Practices

**Mark A. Franklin, Patrick
Crowley, Haldun Hadimioglu**



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processors Programmable yet application specific their designs are tailored to efficiently implement communications applications such as routing protocol analysis voice and data convergence firewalls VPNs and QoS Network processor design is an emerging field with issues and opportunities both numerous and formidable To help meet this challenge the editors of this volume created the first Workshop on Network Processors a forum for scientists and engineers from academia and industry to discuss their latest research in the architecture design programming and use of these devices In addition to including the results of the Workshop in this volume the editors also present specially commissioned material from practicing designers who discuss their companies latest network processors Network Processor Design Issues and Practices is an essential reference on network processors for graduate students researchers and practicing designers Includes contributions from major academic and industrial research labs including Aachen University of Technology Cisco Systems Infineon Technologies Intel Corp North Carolina State University Swiss Federal Institute of Technology University of California Berkeley University of Dortmund University of Washington and Washington University Examines the latest network processors from Agere Systems Cisco IBM Intel Motorola Sierra Inc and TranSwitch

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