

# **SEMICONDUCTOR DEVICE MODELING FOR VLSI**



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**KWYRO LEE  
MICHAEL SHUR  
TOR A. FJELDLY  
TROND YTTERDAL**

# Semiconductor Device Modeling For Vlsi

**Massimo Rudan, Rossella  
Brunetti, Susanna Reggiani**



## **Semiconductor Device Modeling For Vlsi:**

**Semiconductor Device Modeling for VLSI** Rachel Lee, Michael Shur, 1993-08 *Semiconductor Device Modeling for VLSI* Kwyro Lee, 1993-01-01 Explains basic semiconductor physics and looks at bipolar junction metal oxide semiconductor field effect and compound semiconductor field effect transistors thin film transistors and circuit simulation MOSFET Models for VLSI Circuit Simulation Narain D. Arora, 2012-12-06 Metal Oxide Semiconductor MOS transistors are the basic building block of MOS integrated circuits I C Very Large Scale Integrated VLSI circuits using MOS technology have emerged as the dominant technology in the semiconductor industry Over the past decade the complexity of MOS IC s has increased at an astonishing rate This is realized mainly through the reduction of MOS transistor dimensions in addition to the improvements in processing Today VLSI circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0.5 microns are in volume production Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator This problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models There is extensive literature available on modeling these short channel devices However there is a lot of confusion too Often it is not clear what model to use and which model parameter values are important and how to determine them After working over 15 years in the field of semiconductor device modeling I have felt the need for a book which can fill the gap between the theory and the practice of MOS transistor modeling This book is an attempt in that direction *Semiconductor Device Modeling for VLSI* Kwyro Lee, 1993 Analysis and Simulation of Semiconductor Devices S. Selberherr, 2012-12-06 The invention of semiconductor devices is a fairly recent one considering classical time scales in human life The bipolar transistor was announced in 1947 and the MOS transistor in a practically usable manner was demonstrated in 1960 From these beginnings the semiconductor device field has grown rapidly The first integrated circuits which contained just a few devices became commercially available in the early 1960s Immediately thereafter an evolution has taken place so that today less than 25 years later the manufacture of integrated circuits with over 400 000 devices per single chip is possible Coincident with the growth in semiconductor device development the literature concerning semiconductor device and technology issues has literally exploded In the last decade about 50 000 papers have been published on these subjects The advent of so called Very Large Scale Integration VLSI has certainly revealed the need for a better understanding of basic device behavior The miniaturization of the single transistor which is the major prerequisite for VLSI nearly led to a breakdown of the classical models of semiconductor devices **Mosfet Modeling For Vlsi Simulation: Theory And Practice** Narain Arora, 2007-02-14 A reprint of the classic text this book popularized compact modeling of electronic and semiconductor devices and components for college and graduate school classrooms and manufacturing engineering over a

decade ago The first comprehensive book on MOS transistor compact modeling it was the most cited among similar books in the area and remains the most frequently cited today The coverage is device physics based and continues to be relevant to the latest advances in MOS transistor modeling This is also the only book that discusses in detail how to measure device model parameters required for circuit simulations The book deals with the MOS Field Effect Transistor MOSFET models that are derived from basic semiconductor theory Various models are developed ranging from simple to more sophisticated models that take into account new physical effects observed in submicron transistors used in today s 1993 MOS VLSI technology The assumptions used to arrive at the models are emphasized so that the accuracy of the models in describing the device characteristics are clearly understood Due to the importance of designing reliable circuits device reliability models are also covered Understanding these models is essential when designing circuits for state of the art MOS ICs

**Device Modeling for Analog and RF CMOS Circuit Design** Trond Ytterdal, Yuhua Cheng, Tor A. Fjeldly, 2003-08-01 Bridges the gap between device modelling and analog circuit design Includes dedicated software enabling actual circuit design Covers the three significant models BSIM3 Model 9 and EKV Presents practical guidance on device development and circuit implementation The authors offer a combination of extensive academic and industrial experience

**Technology Computer Aided Design** Chandan Kumar Sarkar, 2018-09-03 Responding to recent developments and a growing VLSI circuit manufacturing market Technology Computer Aided Design Simulation for VLSI MOSFET examines advanced MOSFET processes and devices through TCAD numerical simulations The book provides a balanced summary of TCAD and MOSFET basic concepts equations physics and new technologies related to TCAD and MOSFET A firm grasp of these concepts allows for the design of better models thus streamlining the design process saving time and money This book places emphasis on the importance of modeling and simulations of VLSI MOS transistors and TCAD software Providing background concepts involved in the TCAD simulation of MOSFET devices it presents concepts in a simplified manner frequently using comparisons to everyday life experiences The book then explains concepts in depth with required mathematics and program code This book also details the classical semiconductor physics for understanding the principle of operations for VLSI MOS transistors illustrates recent developments in the area of MOSFET and other electronic devices and analyzes the evolution of the role of modeling and simulation of MOSFET It also provides exposure to the two most commercially popular TCAD simulation tools Silvaco and Sentaurus Emphasizes the need for TCAD simulation to be included within VLSI design flow for nano scale integrated circuits Introduces the advantages of TCAD simulations for device and process technology characterization Presents the fundamental physics and mathematics incorporated in the TCAD tools Includes popular commercial TCAD simulation tools Silvaco and Sentaurus Provides characterization of performances of VLSI MOSFETs through TCAD tools Offers familiarization to compact modeling for VLSI circuit simulation R D cost and time for electronic product development is drastically reduced by taking advantage of TCAD tools making it indispensable for modern VLSI

device technologies They provide a means to characterize the MOS transistors and improve the VLSI circuit simulation procedure The comprehensive information and systematic approach to design characterization fabrication and computation of VLSI MOS transistor through TCAD tools presented in this book provides a thorough foundation for the development of models that simplify the design verification process and make it cost effective Bipolar transistor and MOSFET device models Kunihiro Suzuki,2016-03-02 Continuous efforts to develop new semiconductor devices enable device manufacturers to make significant improvements in the information technology sector Bipolar transistors and MOSFETS are two special electronic device components that are used to construct very large scale integrated VLSI circuits allowing engineers to create powerful machines that are power efficient VLSI device characterization depends largely on semiconductor device modeling which is based on physical and electronic principles Bipolar transistor and MOSFET device models is a textbook that describes basic functions and characterization models of these two types of transistors Readers will learn about the processes employed to derive these models which will help them understand the modeling process Chapters in this text cover the fundamentals of semiconductor devices the pn junction high and low injection region models for bipolar transistors and different MOSFET models such as channel doping models and gated SOI models Key features of this book include step by step easy to understand presentation of model information on innovative semiconductor devices an overview of model derivation assumptions approximations and limitations novel experimental information on semiconductor parameters such as gate fringe capacitance silicided source drain resistance and threshold voltage shift Bipolar transistor and MOSFET device models is an essential learning resource for advanced students and professional engineers involved in semiconductor device modeling and fabrication divisions **Advanced Device Modeling And Simulation** Tibor Grasser,2003-10-17 Microelectronics is one of the most rapidly changing scientific fields today The tendency to shrink devices as far as possible results in extremely small devices which can no longer be described using simple analytical models This book covers various aspects of advanced device modeling and simulation As such it presents extensive reviews and original research by outstanding scientists The bulk of the book is concerned with the theory of classical and quantum mechanical transport modeling based on macroscopic spherical harmonics and Monte Carlo methods Semiconductor Device Modelling Christopher M. Snowden,2012-12-06 Semiconductor device modelling has developed in recent years from being solely the domain of device physicists to span broader technological disciplines involved in device and electronic circuit design and development The rapid emergence of very high speed high density integrated circuit technology and the drive towards high speed communications has meant that extremely small scale device structures are used in contemporary designs The characterisation and analysis of these devices can no longer be satisfied by electrical measurements alone Traditional equivalent circuit models and closed form analytical models cannot always provide consistently accurate results for all modes of operation of these very small devices Furthermore the highly competitive nature of the semiconductor industry has

led to the need to minimise development costs and lead time associated with introducing new designs This has meant that there has been a greater demand for models capable of increasing our understanding of how these devices operate and capable of predicting accurate quantitative results The desire to move towards computer aided design and expert systems has reinforced the need for models capable of representing device operation under DC small signal large signal and high frequency operation It is also desirable to relate the physical structure of the device to the electrical performance This demand for better models has led to the introduction of improved equivalent circuit models and a upsurge in interest in using physical models

Computational Aspects of VLSI Design with an Emphasis on Semiconductor Device Simulation Randolph E. Bank, 1990-02-15 Numerical simulation is rapidly becoming an important part of the VLSI design process allowing the engineer to test evaluate and optimize various aspects of chip design without resorting to the costly and time consuming process of fabricating prototypes This procedure not only accelerates the design process but also improves the end product since it is economically feasible to numerically simulate many more options than might otherwise be considered With the enhanced computing power of today's computers more sophisticated models are now being developed This volume contains the proceedings of the AMS SIAM Summer Seminar on Computational Aspects of VLSI Design held at the Institute for Mathematics and Its Applications at the University of Minnesota in the spring of 1987 The seminar featured presentations by some of the top experts working in this area Their contributions to this volume form an excellent overview of the mathematical and computational problems arising in this area

**Springer Handbook of Semiconductor Devices** Massimo Rudan, Rossella Brunetti, Susanna Reggiani, 2022-11-10 This Springer Handbook comprehensively covers the topic of semiconductor devices embracing all aspects from theoretical background to fabrication modeling and applications Nearly 100 leading scientists from industry and academia were selected to write the handbook's chapters which were conceived for professionals and practitioners material scientists physicists and electrical engineers working at universities industrial R D and manufacturers Starting from the description of the relevant technological aspects and fabrication steps the handbook proceeds with a section fully devoted to the main conventional semiconductor devices like e.g bipolar transistors and MOS capacitors and transistors used in the production of the standard integrated circuits and the corresponding physical models In the subsequent chapters the scaling issues of the semiconductor device technology are addressed followed by the description of novel concept based semiconductor devices The last section illustrates the numerical simulation methods ranging from the fabrication processes to the device performances Each chapter is self contained and refers to related topics treated in other chapters when necessary so that the reader interested in a specific subject can easily identify a personal reading path through the vast contents of the handbook

*Modern Semiconductor Device Physics* S. M. Sze, 1998 An in depth up to date presentation of the physics and operational principles of all modern semiconductor devices The companion volume to Dr Sze's classic *Physics of Semiconductor Devices* *Modern Semiconductor Device Physics* covers all the significant

advances in the field over the past decade To provide the most authoritative state of the art information on this rapidly developing technology Dr Sze has gathered the contributions of world renowned experts in each area Principal topics include bipolar transistors compound semiconductor field effect transistors MOSFET and related devices power devices quantum effect and hot electron devices active microwave diodes high speed photonic devices and solar cells Supported by hundreds of illustrations and references and a problem set at the end of each chapter Modern Semiconductor Device Physics is the essential text reference for electrical engineers physicists material scientists and graduate students actively working in microelectronics and related fields

**Dispersive Transport Equations and Multiscale Models** Ben Abdallah Naoufel, Anton Arnold, Pierre Degond, Irene M. Gamba, Robert T. Glassey, C. David Levermore, Christian Ringhofer, 2012-12-06 IMA Volumes 135 Transport in Transition Regimes and 136 Dispersive Transport Equations and Multiscale Models focus on the modeling of processes for which transport is one of the most complicated components This includes processes that involve a wide range of length scales over different spatio temporal regions of the problem ranging from the order of mean free paths to many times this scale Consequently effective modeling techniques require different transport models in each region The first issue is that of finding efficient simulations techniques since a fully resolved kinetic simulation is often impractical One therefore develops homogenization stochastic or moment based subgrid models Another issue is to quantify the discrepancy between macroscopic models and the underlying kinetic description especially when dispersive effects become macroscopic for example due to quantum effects in semiconductors and superfluids These two volumes address these questions in relation to a wide variety of application areas such as semiconductors plasmas fluids chemically reactive gases etc

**Process and Device Modeling** Walter L. Engl, 1986 This book is the first of a new seven volume series which aims to provide a comprehensive description of basic methods and technologies related to CAD for VLSI The series includes up to date results and latest developments with a good balance between theoretical and practical aspects of VLSI design In this volume emphasis is placed on the basics of modeling the opening chapters being devoted to fundamental process and device modeling The following chapters cover different aspects of device modeling and also bridge to process simulation on the one side and circuit simulation on the other A systems approach to physical modeling spanning the whole range of topics covered is also dealt with Recent conferences on the subject have signalled that physical modeling combined with technology device and circuit optimization will undoubtedly become a major trend in the future

**Introduction to Device Modeling and Circuit Simulation** Tor A. Fjeldly, Trond Ytterdal, Michael S. Shur, 1998 This book is a useful reference for practicing electrical engineers as well as a textbook for a junior senior or graduate level course in electrical engineering The authors combine two subjects device modeling and circuit simulation by providing a large number of well prepared examples of circuit simulations immediately following the description of many device models

**Semiconductor Device Modeling with SPICE** Paolo Antognetti, Giuseppe Massobrio, 1988 Semiconductor Device Modeling for Computer-aided Design Gerald J.

Herskowitz, Ronald B. Schilling, 1972      *Mathematical Problems in Semiconductor Physics* P A Marcati, P A  
Markowich, Roberto Natalini, 1995-12-15 This collection of papers arises from a workshop held at the Istituto per le  
Applicazioni del Calcolo of the Italian CNR The first part of the book includes the material covered by three mini series of  
lectures at graduate level on some advanced mathematical topics in semiconductor physics The second part of the book  
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