

Ying Fu

# Physical Models of Semiconductor Quantum Devices

Second Edition

 Springer

# Physical Models For Semiconductor Devices

**Dharmendra Singh Yadav, Shiromani  
Balmukund Rahi, Sukeshni Tirkey**

## **Physical Models For Semiconductor Devices:**

Physical Models for Semiconductor Devices J.E. Carroll, 1980      Physical Models for Semiconductor Devices John E. Carroll, 1974      Physical Models of Semiconductor Quantum Devices Ying Fu, Magnus Willander, 2013-11-27

Solid state electronics is undergoing rapid changes driven by heteroepitaxy lithography and new device concepts. While ten years ago Si was the material of choice in solid state electronics, now GaAs, InGaAs, AlAs, InP, Ge, etc. have all become quite important. The advent of semiconductor lasers and integrated optoelectronic circuits has led to a flurry of activities in compound semiconductors. Additionally, the remarkable advances in thin film epitaxy have allowed active semiconductor devices with sub-three-dimensional properties and built-in controlled biaxial strain due to lattice mismatch. This book addresses three main areas of interest: i) electronic and optical properties of low-dimensional semiconductor materials; ii) principal physics of quantum electronic devices; iii) principal physics of quantum optical devices. These areas will provide readers with an intimate knowledge of the new material properties on which novel solid state electronic devices such as quantum diode and small size transistor, high electron mobility transistor, are based, leading to the very front of the development of material and device research. The link between basic physics on which the real devices are based and the output from the real devices is closely observed in the book.

**Chapter 1: Elemental and compound semiconductors**

1.1 Crystalline nature of solids

The intrinsic property of a crystal is that the environment around a given atom or group of atoms is exactly the same as the environment around another atom or similar group of atoms.

**Physical Models of Semiconductor Quantum Devices** Ying Fu, 2013-09-30      Advanced Physical Models for Silicon Device Simulation Andreas Schenk, 1998-07-07

From the reviews, this is a well-produced book written in an easy-to-read style and will also be a very useful primer for someone starting out the field and a useful source of reference for experienced users.

**Semiconductor Device Modelling** Christopher M. Snowden, 2012-12-06

Semiconductor device modelling has developed in recent years from being solely the domain of device physicists to span broader technological disciplines involved in device and electronic circuit design and development. The rapid emergence of very high speed, high density integrated circuit technology and the drive towards high speed communications has meant that extremely small scale device structures are used in contemporary designs. The characterisation and analysis of these devices can no longer be satisfied by electrical measurements alone. Traditional equivalent circuit models and closed form analytical models cannot always provide consistently accurate results for all modes of operation of these very small devices. Furthermore, the highly competitive nature of the semiconductor industry has led to the need to minimise development costs and lead time associated with introducing new designs. This has meant that there has been a greater demand for models capable of increasing our understanding of how these devices operate and capable of predicting accurate quantitative results. The desire to move towards computer-aided design and expert systems has reinforced the need for models capable of representing device operation under DC, small signal, large signal, and high

frequency operation It is also desirable to relate the physical structure of the device to the electrical performance This demand for better models has led to the introduction of improved equivalent circuit models and a upsurge in interest in using physical models

*Parameter Extraction and Complex Nonlinear Transistor Models* Gunter Kompa,2019-12-31 All model parameters are fundamentally coupled together so that directly measured individual parameters although widely used and accepted may initially only serve as good estimates This comprehensive resource presents all aspects concerning the modeling of semiconductor field effect device parameters based on gallium arsenide GaAs and gallium nitride GaN technology Metal semiconductor field effect transistors MESFETs high electron mobility transistors HEMTs and heterojunction bipolar transistors HBTs their structures and functions and existing transistor models are also classified The Shockley model is presented in order to give insight into semiconductor field effect transistor FET device physics and explain the relationship between geometric and material parameters and device performance Extraction of trapping and thermal time constants is discussed A special section is devoted to standard nonlinear FET models applied to large signal measurements including static pulsed DC and single two tone stimulation High power measurement setups for signal waveform measurement wideband source load pull measurement including envelope source load pull are also included along with high power intermodulation distortion IMD measurement setup including envelope load pull Written by a world renowned expert in the field this book is the first to cover of all aspects of semiconductor FET device modeling in a single volume

*Physics of Semiconductors* Aditya Saxena,2025-02-20 *Physics of Semiconductors Core Principles* is a comprehensive guide that demystifies how semiconductors function from the fundamental physics to the devices we use daily We cater to a general audience with a focus on readers in the United States We begin with the basics of quantum mechanics and solid state physics before diving into how these principles apply to semiconductors like silicon and gallium arsenide We explain crucial concepts such as band theory the flow of electricity through semiconductors and their use in devices like transistors and solar cells Additionally we discuss the manufacturing processes of semiconductors and highlight the advancements scientists are making in developing new and improved semiconductors *Physics of Semiconductors Core Principles* is an excellent resource for anyone eager to understand the intricacies of this essential technology

**Springer Handbook of Semiconductor Devices** Massimo Rudan,Rossella Brunetti,Susanna Reggiani,2022-11-10 This Springer Handbook comprehensively covers the topic of semiconductor devices embracing all aspects from theoretical background to fabrication modeling and applications Nearly 100 leading scientists from industry and academia were selected to write the handbook s chapters which were conceived for professionals and practitioners material scientists physicists and electrical engineers working at universities industrial R D and manufacturers Starting from the description of the relevant technological aspects and fabrication steps the handbook proceeds with a section fully devoted to the main conventional semiconductor devices like e g bipolar transistors and MOS capacitors and transistors used in the production of the standard

integrated circuits and the corresponding physical models In the subsequent chapters the scaling issues of the semiconductor device technology are addressed followed by the description of novel concept based semiconductor devices The last section illustrates the numerical simulation methods ranging from the fabrication processes to the device performances Each chapter is self contained and refers to related topics treated in other chapters when necessary so that the reader interested in a specific subject can easily identify a personal reading path through the vast contents of the handbook

**Quantum Transport in Ultrasmall Devices** David K. Ferry, Harold L. Grubin, Carlo Jacoboni, A.-P. Jauho, 2012-12-06 The operation of semiconductor devices depends upon the use of electrical potential barriers such as gate depletion in controlling the carrier densities electrons and holes and their transport Although a successful device design is quite complicated and involves many aspects the device engineering is mostly to devise a best device design by defining optimal device structures and manipulating impurity profiles to obtain optimal control of the carrier flow through the device This becomes increasingly difficult as the device scale becomes smaller and smaller Since the introduction of integrated circuits the number of individual transistors on a single chip has doubled approximately every three years As the number of devices has grown the critical dimension of the smallest feature such as a gate length which is related to the transport length defining the channel has consequently declined The reduction of this design rule proceeds approximately by a factor of 1.4 each generation which means we will be using 0.1015  $\mu\text{m}$  rules for the 4 Gb chips a decade from now If we continue this extrapolation current technology will require 30 nm design rules and a cell 3.2 size

**Light-Driven Materials and Devices - Fundamentals and Emerging Applications** Murat Kabatas, 2025-07-02 This book brings together key concepts and case studies that showcase the evolving role of semiconductors in optoelectronic devices and clean energy technologies It concisely explores materials and methods relevant to photodiodes photodetectors and photocatalytic systems Topics include a simulation based understanding of GaAs PIN photodiodes the fundamentals and historical context of semiconductors and insights into advanced photovoltaic detectors including avalanche structures for enhanced sensitivity The book also highlights recent approaches in the photodeposition of co catalysts over 2D heterostructures to improve hydrogen production and solar water splitting performance With a balance of foundational knowledge and current research trends this volume offers practical insights into device design light matter interactions and nanomaterial integration It is intended for researchers and engineers in materials science electronics and renewable energy The content is accessible well organized and suitable as a reference or learning resource for those interested in the intersection of semiconductors photonics and sustainable technologies

**Stress and Strain Engineering at Nanoscale in Semiconductor Devices** Chinmay K. Maiti, 2021-06-29 Anticipating a limit to the continuous miniaturization More Moore intense research efforts are being made to co integrate various functionalities More than Moore in a single chip Currently strain engineering is the main technique used to enhance the performance of advanced semiconductor devices Written from an engineering applications standpoint this book

encompasses broad areas of semiconductor devices involving the design simulation and analysis of Si heterostructure silicongermanium SiGe and III N compound semiconductor devices The book provides the background and physical insight needed to understand the new and future developments in the technology CAD TCAD design at the nanoscale Features Covers stressstrain engineering in semiconductor devices such as FinFETs and III V Nitride based devices Includes comprehensive mobility model for strained substrates in global and local strain techniques and their implementation in device simulations Explains the development of strain stress relationships and their effects on the band structures of strained substrates Uses design of experiments to find the optimum process conditions Illustrates the use of TCAD for modeling strain engineered FinFETs for DC and AC performance predictions This book is for graduate students and researchers studying solid state devices and materials microelectronics systems and controls power electronics nanomaterials and electronic materials and devices *III-V Microelectronics* J.P. Nougier,2014-05-27 As is well known Silicon widely dominates the market of semiconductor devices and circuits and in particular is well suited for Ultra Large Scale Integration processes However a number of III V compound semiconductor devices and circuits have recently been built and the contributions in this volume are devoted to those types of materials which offer a number of interesting properties Taking into account the great variety of problems encountered and of their mutual correlations when fabricating a circuit or even a device most of the aspects of III V microelectronics from fundamental physics to modelling and technology from materials to devices and circuits are reviewed Containing contributions from European researchers of international repute this volume is the definitive reference source for anyone interested in the latest advances and results of current experimental research in III V microelectronics **The Physics of Semiconductor Devices** D. A. Fraser,1986 This text explains the effects employed in each class of device used in electronics and derives formulae describing the measurable voltages and currents The new edition contains much updated material and is heavily illustrated with diagrams throughout Compound Semiconductor Device Modelling Christopher M. Snowden,Robert E. Miles,2012-12-06 Compound semiconductor devices form the foundation of solid state microwave and optoelectronic technologies used in many modern communication systems In common with their low frequency counterparts these devices are often represented using equivalent circuit models but it is often necessary to resort to physical models in order to gain insight into the detailed operation of compound semiconductor devices Many of the earliest physical models were indeed developed to understand the unusual phenomena which occur at high frequencies Such was the case with the Gunn and IMPATI diodes which led to an increased interest in using numerical simulation methods Contemporary devices often have feature sizes so small that they no longer operate within the familiar traditional framework and hot electron or even quantum mechanical models are required The need for accurate and efficient models suitable for computer aided design has increased with the demand for a wider range of integrated devices for operation at microwave millimetre and optical frequencies The apparent complexity of equivalent circuit and physics based

models distinguishes high frequency devices from their low frequency counterparts Over the past twenty years a wide range of modelling techniques have emerged suitable for describing the operation of compound semiconductor devices This book brings together for the first time the most popular techniques in everyday use by engineers and scientists The book specifically addresses the requirements and techniques suitable for modelling GaAs InP ternary and quaternary semiconductor devices found in modern technology

**Analytical and Computer-aided Models for III-V Compound Semiconductor Devices** An-Jui Shey,1990 Advanced Field-Effect Transistors Dharmendra Singh Yadav,Shiromani Balmukund Rahi,Sukeshni Tirkey,2023-12-22 Advanced Field Effect Transistors Theory and Applications offers a fresh perspective on the design and analysis of advanced field effect transistor FET devices and their applications The text emphasizes both fundamental and new paradigms that are essential for upcoming advancement in the field of transistors beyond complementary metal oxide semiconductors CMOS This book uses lucid intuitive language to gradually increase the comprehension of readers about the key concepts of FETs including their theory and applications In order to improve readers learning opportunities Advanced Field Effect Transistors Theory and Applications presents a wide range of crucial topics Design and challenges in tunneling FETs Various modeling approaches for FETs Study of organic thin film transistors Biosensing applications of FETs Implementation of memory and logic gates with FETs The advent of low power semiconductor devices and related implications for upcoming technology nodes provide valuable insight into low power devices and their applicability in wireless biosensing and circuit aspects As a result researchers are constantly looking for new semiconductor devices to meet consumer demand This book gives more details about all aspects of the low power technology including ongoing and prospective circumstances with fundamentals of FET devices as well as sophisticated low power applications

**Modeling of high-speed and high-power semiconductor devices** Mikael Andersson,1994 Fifth European Conference on Power Electronics and Applications: Materials and devices ,1993 An Introductory Guide to EC Competition Law and Practice Valentine Korah,1994

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